

**AMENDMENT TO SPECIFICATION:**

Please amend the paragraph starting at page 7, line 20, as follows:

Fig. 1 shows a total block configuration of units in an embodiment of the present invention. A main control unit 10 has a CPU 11 and a communication control section 12, and an input/output unit 20, which has a MPU 21, the communication control section 12 and a mechanism section 22. The mechanism section 22 has, for instance, a motor 24, a sensor 25, etc. Also, a communication control section 12 of the main control unit 10, and a communication control section 12 of an input/output unit 20 have, as described later, the same configuration, and each of these sections has a sending control section 121 and a receiving control part 122. Therefore, the main control unit 10 and the input/output unit 20 can concurrently perform the functions of both of a sending side unit and a receive side unit. In addition, the main control unit 10 can have a plurality of the communication control sections 12, and each of the communication control sections 12 of the main control unit 10 individually communicates with a different communication control section 12 of the input/output unit 20 (not noted in the drawing).